

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Hiroyuki KOBAYASHI, et al.

SERIAL NO: New Application

GAU: Unassigned

FILED: Herewith

EXAMINER: Unassigned

FOR: SEMICONDUCTOR MEMORY DEVICE

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- ☐ The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references were cited or provided in parent application serial no. 10/154,967 filed May 28, 2002, as were either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- ☐ Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

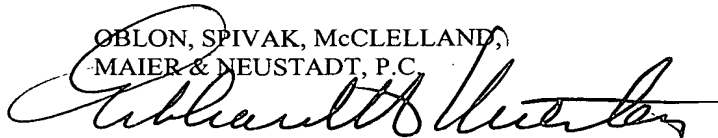
- ☐ Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- ☐ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

- ☐ Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
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Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 248634US2DIV		SERIAL NO. New Applicaiton	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT Hiroyuki KOBAYASHI, et al.			
				FILING DATE Herewith		GROUP Unassigned	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA	5,708,599 A	01/1998	Sato et al			
	AB	5,524,095 A	06/1996	Someya et al			
	AC	6,177,694 B1	01/2001	El Hajji, Noureddine			
	AD	6,115,296 A	09/2000	Ando, Yasuhiro			
	AE	6,046,627 A	04/2000	Itoh et al			
	AF	5,953,261 A	09/1999	Furutani et al			
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO		
	AO						
	AP						
	AQ						
	AR						
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
	AW	Fariborz ASSADERAGHI, et al. "A Dynamic Threshold Voltage MOSFET (DTMOS) for Very Low voltage Operation", IEEE ELECTRON DEVICE LETTERS, Vol. 15, No. 12, Dec. 1994, pgs 510-512					
	AX	In-Young Chung, et al. "A New SOI Inverter Using Dynamic Thershold for Low-Power Applications", IEEE ELECTRON DEVICE LETTERS, Vol. 18, No. 6, June 1997, pgs 248-250					
	AY						
	AZ					<input type="checkbox"/> Additional References sheet(s) attached	
Examiner					Date Considered		
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							